

# 1.5A,50-1000V Fast Recovery Rectifiers

### **Features**

- Low leakage current
- Low forward voltage drop
- Glass passivated chip junction
- For general purpose applications
- Moisture sensitivity: level 1, per J-STD-020
- For fast switching and low logic level applications
- High temperature soldering guaranteed: 260°C/10 seconds



DO-41/DO-15

# **Applications**

• Small battery charger, Power supplies

Maximum Ratings & Electrical Characteristics(TA=25°C unless otherwise noted)									
Parameter	Symbol	FR151G	FR152G	FR153G	FR154G	FR155G	FR156G	FR157G	Unit
Maximum repetitive peak reverse voltage	Vrrm	50	100	200	400	600	800	1000	V
Maximum RMS voltage	Vrms	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	VDC	50	100	200	400	600	800	1000	V
Maximum average forward rectified current	lf(AV)	1.5					A		
Peak forward surge current,8.3ms single half sine- wave superimposed on rated load per diode	IFSM	50				A			
Operating junction temperature range	TJ	-55 to +150			°C				
Storage temperature range	T <sub>STG</sub>	-55 to +150			°C				

Thermal-Mechanical Specifications (TA=25°C unless otherwise noted)							
Parameter	Symbol	Тур	Unit				
Thermal Resistance, Junction to Ambient	Reja	65	°C /W				
Thermal Resistance, Junction to Case	R <sub>ejc</sub>	26	°C /W				
Thermal Resistance, Junction to Lead	R <sub>θJL</sub>	20	°C /W				



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Electrical Specifications(TA=25°C unless otherwise noted)										
Parameter	Symbol	Test Conditions	FR151G	FR152G	FR153G	FR154G	FR155G	FR156G	FR157G	Unit
Forward Drop Voltage	VF	I⊧=1.5A	1.30				V			
Reverse	T <sub>J</sub> =25°C 5							– uA		
leakage I <sub>R</sub> current @V <sub>R</sub>	T」=125℃	100								
Typical junction capacitance	CJ	4.0 V 1 MHZ	12				pF			
Maximum reverse recovery	trr	I <sub>F</sub> =0.5A, I <sub>R</sub> =1.0A,	150 250 500					nS		
time		I <sub>RR</sub> =0.25A								

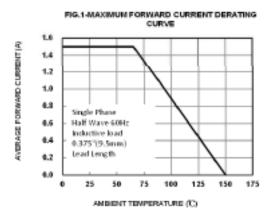
Note:

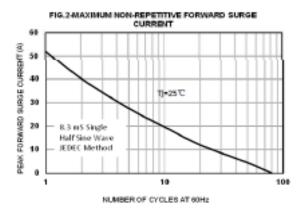
1. Valid provided that leads at a distance of 9.5 mm from case are kept at ambient temperature.



## **Ratings and Characteristics Curves**

(TA = 25°C unless otherwise noted)





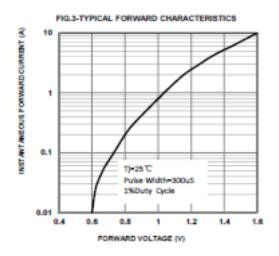
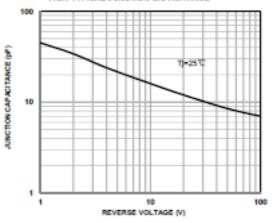
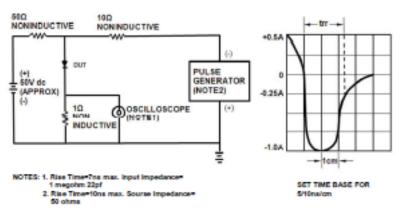


FIG.4-TYPICAL JUNCTION CAPACITANCE



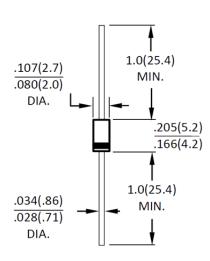
#### FIG.5 - REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM





## Package Outline Dimensions

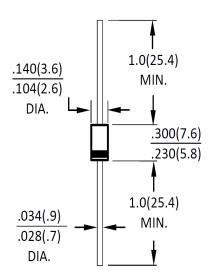
in inches (millimeters)



**DO-41** 

Dimensions in inches and (millimeters)





Dimensions in inches and (millimeters)

# **Revision History**

Document Version	Date of release	Description of changes
Rev.A	2021.06.01	Released Datasheet
Rev.B	2023.11.13	Modify document format



# FR151G thru FR157G

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